Speech recognizer optimization and real-time implementation on a multitransputer array

J. Carazo; J. Morán Carrera; S. Alexandres Fernández

Abstract-

This paper is about the real-time implementation of an existing speech recognition system for Spanish. The large vocabulary involved (1, 000 words) imposes a high computational load. A Transputer based multiprocessor system is employed whose size and architecture are configured in order to provide the necessary processing speed.

Index Terms-

Due to copyright restriction we cannot distribute this content on the web. However, clicking on the next link, authors will be able to distribute to you the full version of the paper:

Request full paper to the authors

If you institution has a electronic subscription to Microprocessing and Microprogramming, you can download the paper from the journal website:

Access to the Journal website

Citation:

Alexandres, S.; Carazo, J.; Moran, J. "Speech recognizer optimization and real-time implementation on a multitransputer array", Microprocessing and Microprogramming, vol.34, no.1-5, pp.219-222, February, 1992.